

## Inventor Name Search Result

Your Search was:

Last Name = ORFALI

First Name = MARWAN

| Application#                   | Patent#    | Status | Date Filed | Title   | Inventor Name                                      |
|--------------------------------|------------|--------|------------|---|--|
| <u>10423100</u>                | Not Issued | 020    | 04/25/2003 | APPARATUS AND METHOD FOR ANALYZING PERFORMANCE OF A DATA PROCESSING SYSTEM                                      | ORFALI, MARWAN A.<br><i>Uy. Marwan</i>             |
| <u>09790627</u>                | Not Issued | 030    | 02/16/2001 | METHODS FOR PREDICTING INSTRUCTION EXECUTION EFFICIENCY IN A PROPOSED COMPUTER SYSTEM                           | ORFALI, MARWAN A.<br><i>Totally Diff</i>           |
| <u>09784933</u><br><i>Law</i>  | Not Issued | 030    | 02/16/2001 | METHODS FOR PREDICTING CACHE MEMORY PERFORMANCE IN A PROPOSED COMPUTER SYSTEM                                   | ORFALI, MARWAN A.<br><i>Totally Diff</i>           |
| <u>09747050</u><br><i>2111</i> | Not Issued | 030    | 12/21/2000 | SYSTEM AND METHOD FOR HIGH SPEED, LOW COST ADDRESS AND BUS SIGNAL TRACING                                       | ORFALI, MARWAN A.<br><i>Sim. multi Bus No D.P.</i> |
| <u>09747046</u><br><i>211</i>  | Not Issued | 030    | 12/21/2000 | COORDINATION OF MULTIPLE PROCESSOR BUS TRACINGS FOR ENABLING STUDY OF MULTIPROCESSOR MULTI-BUS COMPUTER SYSTEMS | ORFALI, MARWAN A.<br><i>Sim. No D.P. Alihans</i>   |
| <u>09745813</u>                | Not Issued | 030    | 12/21/2000 | HIGH SPEED PROCESSOR INTERCONNECT TRACING COMPACTION USING SELECTABLE TRIGGERS                                  | ORFALI, MARWAN A.<br><i>Mine</i>                   |

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